

2016 Kaby Lake Platforms Silicon Initialization Code

Release Notes

March 2020

Release Version 3.7.9

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1 Introduction

- Product: Kaby Lake Platforms Silicon Initialization Code
- Developed by: Intel Corporation
- Software version released date: <March/2020>

Note: This document is cumulative and includes information on previous versions. The version information is presented with the newest release first and then regressing through earlier versions.

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of any proprietary rights, and including the warranties of merchantability and fitness for a particular purpose. Intel does not assume any responsibility for any errors which may appear in this code or any responsibility to update it.

Note: This sample code is to be used in accordance with the DCL document that came with the samples. Refer to the DCL for intended purpose of the sample and recommendations for testing that should not be performed with these samples.

Note: **Visual Studio 2013 Update 5 patch is needed.**

Update with release information can be obtained at following link:

<https://support.microsoft.com/en-in/help/3021976/description-of-visual-studio-2013update-5>

1.1 Purpose of Silicon Initialization Code (SiC)

This is sample initialization code for detection and initialization of Processors and PCHs supported on Kaby Lake Platforms. It supports detection and initialization of Kaby Lake Platform Processor (code named Kaby Lake) and Kaby Lake Chipset Family (code named Kaby Lake PCH) and complies with the requirements in the BIOS specification.

The Silicon Initialization Code is intended for use as part of Intel BIOS. An OEM may need to modify the code to meet specific platform designs, error handling features, platform specific BIOS features, or other local requirements.

1.2 Target Customers

The SiC code is intended for use as part of Intel BIOS. The code is compatible with both mobile and desktop products.

1.3 Version Release History

Date	Version	Description
November 2015	0.5.0	<ul style="list-style-type: none">• Initial release• U/Y Alpha
January 2016	0.6.0	<input type="checkbox"/> U/Y Alpha Update
January 2016	0.6.1	<input type="checkbox"/> U/Y Alpha Update
February 2016	0.7.0	<input type="checkbox"/> U/Y Beta
March 2016	0.7.1	<input type="checkbox"/> U/Y Beta Update

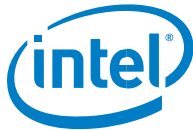


March 2016	0.7.2	<input type="checkbox"/> U/Y Beta Update <input type="checkbox"/>
March 2016	0.7.3	<ul style="list-style-type: none"> • U/Y Beta Update • DT with SPT Alpha Cons/Corp • Halo Alpha Cons/Corp
April 2016	0.8.0	<ul style="list-style-type: none"> • U/Y Beta Update • DT with SPT Alpha Cons/Corp update • Halo Alpha Cons/Corp update
May 2016	0.8.1	<ul style="list-style-type: none"> • U/Y Beta Update • DT with SPT Alpha Cons/Corp update • Halo Alpha Cons/Corp update
May 2016	0.9.0	<ul style="list-style-type: none"> • U/Y PC candidate • DT with SPT Alpha Cons/Corp update • Halo Alpha Cons/Corp update
June 2016	0.9.1	<ul style="list-style-type: none"> • U/Y Production candidate • DT with SPT Alpha Cons/Corp update • Halo Alpha Cons/Corp update
June 2016	1.0.0	<ul style="list-style-type: none"> • U/Y HR'16 RS1 • U23e Alpha • SR'17 S/H/Y/U Beta
June 2016	1.0.1	<ul style="list-style-type: none"> • U/Y HR'16 RS1 • U23e Alpha RS1 • SR'17 S/H/Y/U Beta
July 2016	1.0.2	<input type="checkbox"/> SR'17 U/Y/S/H Beta
July 2016	1.0.3	<input type="checkbox"/> SR'17 U/Y/S/H RS1 Beta update
August 2016	1.0.3.1	<input type="checkbox"/> Update copyright heard/no other change
August 2016	1.0.4.0	<ul style="list-style-type: none"> • Update for PR3 • Update U/Y HR'16 RS1 • Update U23e RS1 • Update SR'17 S/H/Y/U

Version Details

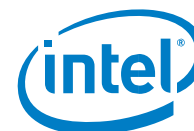


August 2016	1.0.5.0	Update U/Y HR'16 RS1 Update U23e RS1 Update SR'17 S/H/Y/U
September 2016	1.1.0.0	PV U/Y HR'16 RS1 Update SR'17 S/H/Y/U Update U23e RS1
October 2016	1.2.0.0	PV SR'17 S42 – consumer PCV SR'17 Y/U Update PCV SR'17 H/U32e/ all Corp SKUs
October 2016	1.3.0.0	PV SR'17 H – consumer PC/PV* SR'17 S42/H/ and Y/U22 – Corporate* Update to Beta SR'17 S22 with RS1
November 2016	1.4.0.0	PV SKL on KBL Platforms with Win7/Win8.1 Post SR'17 update - PR1 Update to U23e
November 2016	1.4.1.0	Optane enabling update only
November 2016	1.5.0.0	PV SKL on KBL Platforms with Win7/Win8.1 Post SR'17 update – PR2 Update to U23e
January 2017	1.6.0.0	Post SR'17 update for PR2
February 2017	1.7.0.0	Pre-ES KBL-R Post SR'17 update
February 2017	1.8.0.0	KBL-R support Post SR'17 update
March 2017	1.9.0.0	KBL-R Pre-Beta support Post SR'17 update
March 2017	2.0.0.0	KBL-R Beta support Post SR'17 update
April 2017	2.1.0.0	KBL-R Beta support Post SR'17 update
April 2017	2.2.0.0	KBL-R update Post SR'17 update
May 2017	2.3.0.0	Added Coffee Lake-S support
May 2017	2.2.1.0	KBL-R PC/PV KBL RS2 PC/PV See note about this release vs 2.3.0 in version details

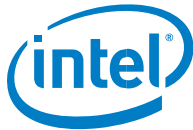


June 2017	2.4.0.0	KBL-R PC/PV KBL RS2 PC/PV CFL-S with Kaby Point PCH Beta Combined 2.2.1 and 2.3.0 release, See note about 2.2.1 release vs 2.3.0 release in version details
June 2017	2.5.0.0	KBL-R PC/PV KBL RS2 PC/PV Aligning with KBL-R PR1 BKC Release.
July 2017	2.5.1.0	KBL/PV KBL-R/ PV CFL-S with Kaby Point PCH Beta Aligning with KBL/KBL-R RS2 corporate PV BKC Release.
July 2017	2.6.0.0	KBL/PV KBL-R/ PV CFL-S with Kaby Point PCH Beta Aligning with CFL-S with Kaby Point PCH QS BKC release
August 2017	2.6.1.0	KBL/PV KBL-R/ PV CFL-S with Kaby Point PCH Beta Aligning with CFL-S with Kaby Point PCH WW31 BKC release
August 2017	2.6.2.0	CFL-S with Kaby Point PCH Consumer PV RS2 KBL/PV KBL-R/ PV Aligning with CFL-S62/S42 + KBP-H PCH Consumer PV BKC release WW32 RS2
August 2017	2.7.0.0	KBL/KBL-R RS3 WW33 BKC CFL-S with Kaby Point PCH Aligning with KBL/KBL-R BKC release WW33 RS3
September 2017	2.7.2.0	KBL/KBL-R RS3 WW36 BKC RS3 CFL-S with Kaby Point PCH PR1 RS2 Aligning with KBL/KBL-R BKC WW36 RS3 & CFL S 62 /CFL S 42 + KBP-H PCH PR1 RS2
October 2017	2.8.0.0	KBL/KBL-R RS3 WW39 BKC Aligning with KBL/KBL-R BKC WW39 RS3
October 2017	2.8.1.0	KBL/KBL-R RS3 WW41 BKC Aligning with KBL/KBL-R BKC WW41 RS3
November 2017	2.9.0.0	KBL/KBL-R RS3 WW44 BKC Aligning with KBL/KBL-R BKC WW44 RS3
November 2017	2.9.2.0	CFL-S with Kaby Point PCH Aligning with CFL-S62/S42 + KBP-H BKC WW47 RS3

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December 2017	3.0.0.0	KBL/KBL-R Aligning with KBL S42/H42 PV, Optane PV for S42/H42/U22/KBL-R, KBL-R WW49 BKC
December 2017	3.0.1.0	KBL/KBL-R PR1 Aligning with KBL-Y22 PV, KBL U22 PV, KBL U23e PV and KBL R PR1 BKC
February 2018	3.1.1.0	CFL-S with Kaby Point PCH Aligning with CFL-S62/S42 + KBP-H Consumer PV BKC WW08 RS3
March 2018	3.1.2.0	CFL-S with Kaby Point PCH Aligning with CFL-S62/42+KBP-H Consumer PR1 BKC WW10 RS3
April 2018	3.2.0.0	KBL/KBL-R/KBL-YR Aligning with KBL-YR RS4 BKC WW15
May 2018	3.2.1.0	KBL/KBL-R/KBL-YR Aligning with KBL-YR RS4 BKC WW17
May 2018	3.3.0.0	KBL/KBL-R/KBL-YR Aligning with KBL-YR RS4 BKC WW21
June 2018	3.5.0.0	KBL/KBL-R/KBL-YR Coffee Lake S82, KBP PCH-H Beta BKC Win10 RS4 WW25'2018
June 2018	3.6.0.0	KBL/KBL-R/KBL-YR KBL-G RS4 Corp BKC release WW26'2018
July 2018	3.6.1.0	KBL/KBL-R/KBL-YR AML-Y22 5W PR1 BKC WW29'2018
August 2018	3.6.1.1	KBL/KBL-R/KBL-YR KBL-R RS4 PV BKC WW32'2018
August 2018	3.6.2	KBL/KBL-R/KBL-YR AML-Y22 7W PV BKC WW31
September 2018	3.6.3	KBL/KBL-R/KBL-YR Coffee Lake S82, Kaby lake PCH-H PV Best Known Configuration (BKC) (Microsoft Windows* 10 64-bit Redstone 4) WW37'2018 KBL-R RS5 BKC WW37'2018
September 2018	3.6.4	AML Y22 5W RS5 Beta WW40'2018
October 2018	3.6.5	Coffee Lake S82/62, Kaby Lake PCH-H Beta Best Known Configuration (BKC) (Microsoft Windows* 10 64-bit Redstone 5) WW43'2018 KBL-R U42 Beta BKC WW43'2018
November 2018	3.6.6	AML Y22 5W RS5 Biweekly BKC WW45
November 2018	3.6.7	Coffee Lake S82/62/42, Kaby Lake PCH-H Consumer Best Known Configuration (BKC) (Microsoft Windows* 10 64-bit Redstone 5) WW45.3'2018
December 2018	3.6.7.1	KBLU42 RS5 PV BKC WW51



January 2019	3.6.7.2	Coffee Lake S82/62/42, Kaby Lake PCH-H, Consumer Production Version (PV) Best Known Configuration (BKC) (Microsoft Windows* 10 64-bit Redstone 5) WW04'2019.
January 2019	3.6.9	AML Y42 RS5 Beta Best Known Configuration (BKC) (Microsoft Windows* 10 – 64bit Redstone 5) WW03'2019.
March 2019	3.7.0	KBL/KBL-R/AML AML Y22/Y42 Beta Best Known Configuration (BKC) Software Package (Microsoft Windows*10 – 64bit 19H1) WW12'2019
March 2019	3.7.1	CFL S82/62/42 Kaby Lake PCH-H Consumer (PV) Best Known Configuration (BKC) (Microsoft Windows* 10 – 64bit Redstone 5) WW13'2019
April 2019	3.7.2	Coffee Lake S82/62/42 Kaby Lake PCH-H Consumer Beta Best Known Configuration (BKC) Software Package (Microsoft Windows* 10 – 64bit 19H1) WW17'2019.
May 2019	3.7.3	KBL/KBL-R/AML AML Y42/Y22 QS, Best Known Configuration (BKC) Software Package (Microsoft Windows* 10 – 64bit 19H1) WW18'2019.
June 2019	3.7.4	KBL/KBL-R/AML AML Y42/Y22 PV, Best Known Configuration (BKC) Software Package (Microsoft Windows* 10 – 64bit 19H1) WW23'2019. (November – 2019 update) Amber Lake Y42 PV (Production Version) Best Known Configuration (BKC) Software Package (Microsoft Windows*10 – 64-bit 19H2) WW46'2019
June 2019	3.7.5	Coffee Lake S82/62/42 KBP PCH-H Consumer Production Version (PV) Best Known Configuration (BKC) Software Package (Microsoft Windows* 10 – 64bit 19H1) WW23'2019.
September 2019	3.7.6	KBL/KBL-R/AML
December 2019	3.7.7	KBL/KBL-R/AML AML-Y42, Best Known Configuration (BKC) Software Package (WCOS 20H1) WW52'2019.
January 2020	3.7.8	KBL/KBL-R/AML AML-Y42, Best Known Configuration (BKC) Software Package (WCOS 20H1) WW03'2020. (February-2020 Update) AML-Y42 (also known as CML-Y) Best Known Configuration (BKC) Windows* 10X (WCOS) Software Package (Microsoft Windows*10X - 64-bit 20H1) WW06'2020.
March 2020	3.7.9	KBL/KBL-R/AML AML-Y42, Best Known Configuration (BKC) Software Package (WCOS 20H1) WW10'2020. (March-2020 new Update) AML-Y42 (also known as CML-Y) Best Known Configuration (BKC) (WCOS) Software Package (Microsoft Windows*10X 64-bit 20H1) WW11'2020.

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2 Version Details

2.1 Version 3.7.9

This release is to support KBL/KBL-R/KBL-YR and is intended for aligning with BIOS build v168 WW10'2020. This release is combine release for CFL-S with Kaby Point, KBL-R, all KBLs and SKL.

This release includes source code (including debug code) and APIs. Integration guide is part of SiPkgApi.chm (Doxygen) file now. Please refer to the help file for all code integration needs.

Refer to [BIOS Specification Information.txt](#) (under Doc folder) for BIOS specifications that support this release.

2.2 Version 3.7.8

This release is to support KBL/KBL-R/KBL-YR and is intended for aligning with BIOS build v166 WW03'2020. This release is combine release for CFL-S with Kaby Point, KBL-R, all KBLs and SKL.

This release includes source code (including debug code) and APIs. Integration guide is part of SiPkgApi.chm (Doxygen) file now. Please refer to the help file for all code integration needs.

Refer to [BIOS Specification Information.txt](#) (under Doc folder) for BIOS specifications that support this release.

2.3 Version 3.7.7

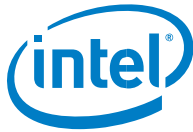
This release is to support KBL/KBL-R/KBL-YR and is intended for aligning with BIOS build v163 WW52'2019. This release is combine release for CFL-S with Kaby Point, KBL-R, all KBLs and SKL.

This release includes source code (including debug code) and APIs. Integration guide is part of SiPkgApi.chm (Doxygen) file now. Please refer to the help file for all code integration needs.

Refer to [BIOS Specification Information.txt](#) (under Doc folder) for BIOS specifications that support this release.

2.4 Version 3.7.6

This release is to support KBL/KBL-R/KBL-YR and is intended for aligning with BIOS build v160 WW36'2019. This release is combine release for CFL-S with Kaby Point, KBL-R, all KBLs and SKL.



This release includes source code (including debug code) and APIs. Integration guide is part of SiPkgApi.chm (Doxygen) file now. Please refer to the help file for all code integration needs.

Refer to [BIOS Specification Information.txt](#) (under Doc folder) for BIOS specifications that support this release.

2.5 **Version 3.7.5**

This release is to support KBL/KBL-R/KBL-YR and is intended for aligning with BIOS build v159 WW23'2019. This release is combine release for CFL-S with Kaby Point, KBL-R, all KBLs and SKL.

This release includes source code (including debug code) and APIs. Integration guide is part of SiPkgApi.chm (Doxygen) file now. Please refer to the help file for all code integration needs.

Refer to [BIOS Specification Information.txt](#) (under Doc folder) for BIOS specifications that support this release.

2.6 **Version 3.7.4**

This release is to support KBL/KBL-R/KBL-YR and is intended for aligning with BIOS build v158.1 WW23'2019. This release is combine release for CFL-S with Kaby Point, KBL-R, all KBLs and SKL.

This release includes source code (including debug code) and APIs. Integration guide is part of SiPkgApi.chm (Doxygen) file now. Please refer to the help file for all code integration needs.

Refer to [BIOS Specification Information.txt](#) (under Doc folder) for BIOS specifications that support this release.

2.7 **Version 3.7.3**

This release is to support KBL/KBL-R/KBL-YR and is intended for aligning with BIOS build v156 WW18'2019. This release is combine release for CFL-S with Kaby Point, KBL-R, all KBLs and SKL.

This release includes source code (including debug code) and APIs. Integration guide is part of SiPkgApi.chm (Doxygen) file now. Please refer to the help file for all code integration needs.

Refer to [BIOS Specification Information.txt](#) (under Doc folder) for BIOS specifications that support this release.



2.8 Version 3.7.2

This release is to support KBL/KBL-R/KBL-YR and is intended for aligning with BIOS build v154 WW17'2019. This release is combine release for CFL-S with Kaby Point, KBL-R, all KBLs and SKL.

This release includes source code (including debug code) and APIs. Integration guide is part of SiPkgApi.chm (Doxygen) file now. Please refer to the help file for all code integration needs.

Refer to [BIOS Specification Information.txt](#) (under Doc folder) for BIOS specifications that support this release.

2.9 Version 3.7.1

This release is to support KBL/KBL-R/KBL-YR and is intended for aligning with BIOS build v153 WW13'2019. This release is combine release for CFL-S with Kaby Point, KBL-R, all KBLs and SKL.

This release includes source code (including debug code) and APIs. Integration guide is part of SiPkgApi.chm (Doxygen) file now. Please refer to the help file for all code integration needs.

Refer to [BIOS Specification Information.txt](#) (under Doc folder) for BIOS specifications that support this release.

2.10 Version 3.7.0

This release is to support KBL/KBL-R/KBL-YR and is intended for aligning with BIOS build v152 WW12'2019. This release is combine release for CFL-S with Kaby Point, KBL-R, all KBLs and SKL.

This release includes source code (including debug code) and APIs. Integration guide is part of SiPkgApi.chm (Doxygen) file now. Please refer to the help file for all code integration needs.

Refer to [BIOS Specification Information.txt](#) (under Doc folder) for BIOS specifications that support this release.

2.11 Version 3.6.9

This release is to support KBL/KBL-R/KBL-YR and is intended for aligning with AML Y42 RS5 Beta Best Known Configuration (BKC) (Microsoft Windows* 10 64-bit Redstone 5) WW03'2019. This release is combine release for CFL-S with Kaby Point, KBL-R, all KBLs and SKL.

This release includes source code (including debug code) and APIs. Integration guide is part of SiPkgApi.chm (Doxygen) file now. Please refer to the help file for all code integration needs.



Refer to [BIOS Specification Information.txt](#) (under Doc folder) for BIOS specifications that support this release.

2.12 **Version 3.6.7.2**

This release is to support KBL/KBL-R/KBL-YR and is intended for aligning with Coffee Lake S82/62/42, Kaby Lake PCH-H, Consumer Production Version (PV) Best Known Configuration (BKC) (Microsoft Windows* 10 64-bit Redstone 5) WW04'2019. This release is combine release for CFL-S with Kaby Point, KBL-R, all KBLs and SKL.

This release includes source code (including debug code) and APIs. Integration guide is part of SiPkgApi.chm (Doxygen) file now. Please refer to the help file for all code integration needs.

Refer to [BIOS Specification Information.txt](#) (under Doc folder) for BIOS specifications that support this release.

2.13 **Version 3.6.7.1**

This release is to support KBL/KBL-R/KBL-YR and is intended for aligning with KBLU42 PV Best Known Configuration (BKC) (Microsoft Windows* 10 64-bit Redstone 5) WW51'2018. This release is combine release for CFL-S with Kaby Point, KBL-R, all KBLs and SKL.

This release includes source code (including debug code) and APIs. Integration guide is part of SiPkgApi.chm (Doxygen) file now. Please refer to the help file for all code integration needs.

Refer to [BIOS Specification Information.txt](#) (under Doc folder) for BIOS specifications that support this release

2.14 **Version 3.6.7**

This release is to support KBL/KBL-R/KBL-YR and is intended for aligning with Coffee Lake S82/62/42, Kaby Lake PCH-H Consumer Best Known Configuration (BKC) (Microsoft Windows* 10 64-bit Redstone 5) WW45.3'2018. This release is combine release for CFL-S with Kaby Point, KBL-R, all KBLs and SKL.

This release includes source code (including debug code) and APIs. Integration guide is part of SiPkgApi.chm (Doxygen) file now. Please refer to the help file for all code integration needs.

Refer to [BIOS Specification Information.txt](#) (under Doc folder) for BIOS specifications that support this release

2.15 **Version 3.6.6**

This release is to support KBL/KBL-R/KBL-YR and is intended for aligning with AML Y22 5W RS5 Biweekly BKC WW45'2018. This release is combine release for CFL-S with Kaby Point, KBL-R, all KBLs and SKL.



This release includes source code (including debug code) and APIs. Integration guide is part of SiPkgApi.chm (Doxygen) file now. Please refer to the help file for all code integration needs.

Refer to [BIOS Specification Information.txt](#) (under Doc folder) for BIOS specifications that support this release

2.16 Version 3.6.5

This release is to support KBL/KBL-R/KBL-YR and is intended for aligning with Coffee Lake S82/62, Kaby Lake PCH-H Beta Best Known Configuration (BKC) (Microsoft Windows* 10 64-bit Redstone 5) WW43'2018 and KBL-R U42 Beta BKC WW43'2018. This release is combine release for CFL-S with Kaby Point, KBL-R, all KBLs and SKL.

This release includes source code (including debug code) and APIs. Integration guide is part of SiPkgApi.chm (Doxygen) file now. Please refer to the help file for all code integration needs.

Refer to [BIOS Specification Information.txt](#) (under Doc folder) for BIOS specifications that support this release.

2.17 Version 3.6.4

This release is to support KBL/KBL-R/KBL-YR and is intended for aligning with AML Y22 5W RS5 Beta WW40'2018. This release is combine release for CFL-S with Kaby Point, KBL-R, all KBLs and SKL.

This release includes source code (including debug code) and APIs. Integration guide is part of SiPkgApi.chm (Doxygen) file now. Please refer to the help file for all code integration needs.

Refer to [BIOS Specification Information.txt](#) (under Doc folder) for BIOS specifications that support this release.

2.18 Version 3.6.3

This release is to support KBL/KBL-R/KBL-YR and is intended for aligning with Coffee Lake S82, Kaby lake PCH-H PV Best Known Configuration (BKC) (Microsoft Windows* 10 64-bit Redstone 4) WW37'2018 and KBL-R RS5 BKC WW37'2018. This release is combine release for CFL-S with Kaby Point, KBL-R, all KBLs and SKL.

This release includes source code (including debug code) and APIs. Integration guide is part of SiPkgApi.chm (Doxygen) file now. Please refer to the help file for all code integration needs.



Refer to [BIOS Specification Information.txt](#) (under Doc folder) for BIOS specifications that support this release.

2.19 **Version 3.6.2**

This release is to support KBL/KBL-R/KBL-YR and is intended for aligning with AMLY22 7W PV BKC WW31'2018. This release is combine release for CFL-S with Kaby Point, KBL-R, all KBLs and SKL.

This release includes source code (including debug code) and APIs. Integration guide is part of SiPkgApi.chm (Doxygen) file now. Please refer to the help file for all code integration needs.

Refer to [BIOS Specification Information.txt](#) (under Doc folder) for BIOS specifications that support this release.

2.20 **Version 3.6.1.1**

This release is to support KBL/KBL-R/KBL-YR and is intended for aligning with KBL-R RS4 PV BKC WW32'2018. This release is combine release for CFL-S with Kaby Point, KBL-R, all KBLs and SKL.

This release includes source code (including debug code) and APIs. Integration guide is part of SiPkgApi.chm (Doxygen) file now. Please refer to the help file for all code integration needs.

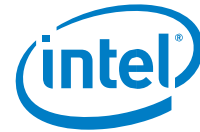
Refer to [BIOS Specification Information.txt](#) (under Doc folder) for BIOS specifications that support this release.

2.21 **Version 3.6.1**

This release is to support KBL/KBL-R/KBL-YR and is intended for aligning with AMLY22 5W PR1 BKC WW29'2018. This release is combine release for CFL-S with Kaby Point, KBL-R, all KBLs and SKL.

This release includes source code (including debug code) and APIs. Integration guide is part of SiPkgApi.chm (Doxygen) file now. Please refer to the help file for all code integration needs.

Refer to [BIOS Specification Information.txt](#) (under Doc folder) for BIOS specifications that support this release.



2.22 Version 3.6.0

This release is to support KBL/KBL-R/KBL-YR and is intended for aligning with KBL-G RS4 Corp BKC release WW26'2018. This release is combine release for CFL-S with Kaby Point, KBL-R, all KBLs and SKL.

This release includes source code (including debug code) and APIs. Integration guide is part of SiPkgApi.chm (Doxygen) file now. Please refer to the help file for all code integration needs.

Refer to [BIOS Specification Information.txt](#) (under Doc folder) for BIOS specifications that support this release.

2.23 Version 3.5.0

This release is to support KBL/KBL-R/KBL-YR and is intended for aligning with Coffee Lake S82, KBP PCH-H Beta BKC Win10 RS4 WW25'2018. This release is combine release for CFL-S with Kaby Point, KBL-R, all KBLs and SKL.

This release includes source code (including debug code) and APIs. Integration guide is part of SiPkgApi.chm (Doxygen) file now. Please refer to the help file for all code integration needs.

Refer to [BIOS Specification Information.txt](#) (under Doc folder) for BIOS specifications that support this release.

2.24 Version 3.3.0

This release is to support KBL/KBL-R/KBL-YR and is intended for aligning with KBL-YR RS4 BKC WW21. This release is combine release for CFL-S with Kaby Point, KBL-R, all KBLs and SKL.

This release includes source code (including debug code) and APIs. Integration guide is part of SiPkgApi.chm (Doxygen) file now. Please refer to the help file for all code integration needs.

Refer to [BIOS Specification Information.txt](#) (under Doc folder) for BIOS specifications that support this release.

2.25 Version 3.2.1

This release is to support KBL/KBL-R/KBL-YR and is intended for aligning with KBL-YR RS4 BKC WW17. This release is combine release for CFL-S with Kaby Point, KBL-R, all KBLs and SKL.

This release includes source code (including debug code) and APIs.



Integration guide is part of SiPkgApi.chm (Doxygen) file now. Please refer to the help file for all code integration needs.

Refer to [BIOS Specification Information.txt](#) (under Doc folder) for BIOS specifications that support this release.

2.26 **Version 3.2.0**

This release is to support KBL/KBL-R/KBL-YR and is intended for aligning with KBL-YR RS4 BKC WW15. This release is combine release for CFL-S with Kaby Point, KBL-R, all KBLs and SKL.

This release includes source code (including debug code) and APIs. Integration guide is part of SiPkgApi.chm (Doxygen) file now. Please refer to the help file for all code integration needs.

Refer to [BIOS Specification Information.txt](#) (under Doc folder) for BIOS specifications that support this release.

2.27 **Version 3.1.2**

This release is to support CFL-S with Kaby Point RS3 consumer PR1 BKC and is intended for aligning with CFL-S62/42+KBP-H Consumer PR1 BKC WW10 RS3. This release is combine release for CFL-S with Kaby Point, KBL-R, all KBLs and SKL.

This release includes source code (including debug code) and APIs. Integration guide is part of SiPkgApi.chm (Doxygen) file now. Please refer to the help file for all code integration needs.

Refer to [BIOS Specification Information.txt](#) (under Doc folder) for BIOS specifications that support this release.

2.28 **Version 3.1.1**

This release is to support CFL-S with Kaby Point RS3 consumer PV BKC and is intended for aligning with CFL-S62/S42 + KBP-H Consumer PV BKC WW08 RS3. This release is combine release for CFL-S with Kaby Point, KBL-R, all KBLs and SKL.

This release includes source code (including debug code) and APIs. Integration guide is part of SiPkgApi.chm (Doxygen) file now. Please refer to the help file for all code integration needs.

Refer to [BIOS Specification Information.txt](#) (under Doc folder) for BIOS specifications that support this release.



2.29 Version 3.0.1

This release is to support KBL/KBL-R BKC and is intended for aligning with KBL-Y22 PV, KBL U22 PV, KBL U23e PV and KBL R PR1 BKC. This release is combine release for KBL-R, all KBLs and SKL.

This release includes source code (including debug code) and APIs. Integration guide is part of SiPkgApi.chm (Doxygen) file now. Please refer to the help file for all code integration needs.

Refer to [BIOS Specification Information.txt](#) (under Doc folder) for BIOS specifications that support this release.

2.30 Version 3.0.0

This release is to support KBL/KBL-R BKC and is intended for aligning with KBL S42/H42 PV, Optane PV for S42/H42/U22/KBL-R, KBL-R WW49 BKC. This release is combine release for KBL-R, all KBLs and SKL.

This release includes source code (including debug code) and APIs. Integration guide is part of SiPkgApi.chm (Doxygen) file now. Please refer to the help file for all code integration needs.

Refer to [BIOS Specification Information.txt](#) (under Doc folder) for BIOS specifications that support this release.

2.31 Version 2.9.2

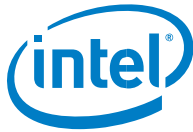
This release is to support CFL-S with Kaby Point RS3 BKC and is intended for aligning with CFL-S62/S42 + KBP-H BKC WW47 RS3. This release is combine release for CFL-S with Kaby Point, KBL-R, all KBLs and SKL.

This release includes source code (including debug code) and APIs. Integration guide is part of SiPkgApi.chm (Doxygen) file now. Please refer to the help file for all code integration needs.

Refer to [BIOS Specification Information.txt](#) (under Doc folder) for BIOS specifications that support this release.

2.32 Version 2.9.0

This release is to support KBL/KBL-R RS3 consumer BKC and is intended for aligning with KBL/KBL-R BKC WW44 RS3. This release is combine release for KBL-R, all KBLs and SKL.



This release includes source code (including debug code) and APIs. Integration guide is part of SiPkgApi.chm (Doxygen) file now. Please refer to the help file for all code integration needs.

Refer to [BIOS Specification Information.txt](#) (under Doc folder) for BIOS specifications that support this release.

2.33 Version 2.8.1

This release is to support KBL/KBL-R RS3 consumer BKC and is intended for aligning with KBL/KBL-R BKC WW41 RS3. This release is combine release for KBL-R, all KBLs and SKL.

This release includes source code (including debug code) and APIs. Integration guide is part of SiPkgApi.chm (Doxygen) file now. Please refer to the help file for all code integration needs.

Refer to [BIOS Specification Information.txt](#) (under Doc folder) for BIOS specifications that support this release.

2.34 Version 2.8.0

This release is to support KBL/KBL-R RS3 consumer BKC and is intended for aligning with KBL/KBL-R BKC WW39 RS3. This release is combine release for KBL-R, all KBLs and SKL.

This release includes source code (including debug code) and APIs. Integration guide is part of SiPkgApi.chm (Doxygen) file now. Please refer to the help file for all code integration needs.

Refer to [BIOS Specification Information.txt](#) (under Doc folder) for BIOS specifications that support this release.

NOTE: An issue of system hang is observed on CS enabled KBL system with RS2 OS with this code. Intel is working to provide the fix in upcoming release (in platform code) to maintain backward compatibility. Hence this code is not recommended to be used in production/upgrade/ manufacturing environment for KBL systems with CS enabled on RS2 OS.

2.35 Version 2.7.2

This release is to support KBL/KBL-R RS3 consumer BKC and is intended for aligning with KBL/KBL-R BKC WW36 RS3 and CFL S 62 /CFL S 42 + KBP-H PCH WW36 PR1 RS2 BKC.

This release is combine release for KBL-R, all KBLs and SKL, CFL-S with Kaby Point.

This release includes source code (including debug code) and APIs.



Integration guide is part of SiPkgApi.chm (Doxygen) file now. Please refer to the help file for all code integration needs.

Refer to [BIOS Specification Information.txt](#) (under Doc folder) for BIOS specifications that support this release.

2.36 Version 2.7.0

This release is to support KBL/KBL-R RS3 consumer BKC and is intended for aligning with KBL/KBL-R consumer BKC release WW33 RS3.

This release is combine release for KBL-R, all KBLs and SKL, CFL-S with Kaby Point.

This release includes source code (including debug code) and APIs.

Integration guide is part of SiPkgApi.chm (Doxygen) file now. Please refer to the help file for all code integration needs.

Refer to [BIOS Specification Information.txt](#) (under Doc folder) for BIOS specifications that support this release.

2.37 Version 2.6.2

This release is to support CFL-S with Kaby Point RS2 consumer PV and is intended for aligning with CFL-S62/S42 + KBP-H PCH Consumer PV BKC release WW32 RS2. This release is combine release for KBL-R PC/PV, all KBLs and SKL for RS2 PC/PV, CFL-S with Kaby Point RS2 Consumer PV.

This release includes source code (including debug code) and APIs.

Integration guide is part of SiPkgApi.chm (Doxygen) file now. Please refer to the help file for all code integration needs.

Refer to [BIOS Specification Information.txt](#) (under Doc folder) for BIOS specifications that support this release.

2.38 Version 2.6.1

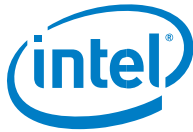
This release is to support CFL-S with Kaby Point post Beta and is intended for aligning with CFL-S with Kaby Point WW31 BKC.

This release is combine release for KBL-R PC/PV, all KBLs and SKL for RS2 PC/PV, CFL-S with Kaby Point post Beta.

This release includes source code (including debug code) and APIs.

Integration guide is part of SiPkgApi.chm (Doxygen) file now. Please refer to the help file for all code integration needs.

Refer to [BIOS Specification Information.txt](#) (under Doc folder) for BIOS specifications that support this release.



2.39 Version 2.6.0

This release is to support CFL-S with Kaby Point post Beta and is intended for aligning with CFL-S with Kaby Point QS BKC.

This release is combine release for KBL-R PC/PV, all KBLs and SKL for RS2 PC/PV, CFL-S with Kaby Point post Beta.

This release includes source code (including debug code) and APIs.

Integration guide is part of SiPkgApi.chm (Doxygen) file now. Please refer to the help file for all code integration needs.

Refer to [BIOS Specification Information.txt](#) (under Doc folder) for BIOS specifications that support this release.

2.40 Version 2.5.1

This release is to support KBL/KLB-R RS2 PV and is intended for aligning with KBL/KBL-R corporate PV release.

This release is combine release for KBL-R PC/PV, all KBLs and SKL for RS2 PC/PV, CFL-S with Kaby Point post Beta.

This release includes source code (including debug code) and APIs.

Integration guide is part of SiPkgApi.chm (Doxygen) file now. Please refer to the help file for all code integration needs.

Refer to [BIOS Specification Information.txt](#) (under Doc folder) for BIOS specifications that support this release.

2.41 Version 2.5.0

This release is to support KBL-R RS2 PC/PV and is intended for aligning with KBL-R PR1 release.

This release is combine release for KBL-R PC/PV, all KBLs and SKL for RS2 PC/PV, CFL-S with Kaby Point post Beta.

This release includes source code (including debug code) and APIs.

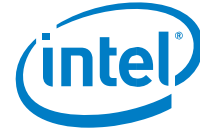
Integration guide is part of SiPkgApi.chm (Doxygen) file now. Please refer to the help file for all code integration needs.

Refer to [BIOS Specification Information.txt](#) (under Doc folder) for BIOS specifications that support this release.

2.42 Version 2.4.0

This release is combine release for KBL-R PC/PV, all KBLs and SKL for RS2 PC/PV, CFL-S with Kaby Point Beta.

Important: Customers coming to KBL-R workshop in Taiwan in ww24/25 are required to have 2.2.1 integrated. Customers also can come with 2.4.0 to workshop if they



have time for integration and wish to use single BIOS. Customers cannot use 2.3.0 for the KBL-R PV or workshop in ww24/ww25 since 2.3.0 doesn't have required KBL-R changes.

This release includes source code (including debug code) and APIs. Integration guide is part of SiPkgApi.chm (Doxygen) file now. Please refer to the help file for all code integration needs.

Refer to [BIOS Specification Information.txt](#) (under Doc folder) for BIOS specifications that support this release.

2.43 Version 2.2.1

This release is KBL-R PC/PV and support for RS2 PC/PV.

This code is based on 2.2.0 version and includes only recommended changes for KBLR PV (including revenue shipped KBL products). Notice that Si Init code 2.3.0 released in ww20 does not contain recommended KBL-R PV changes hence 2.2.1 is not subset of 2.3.0 code.

Intel is releasing Si Init Code 2.2.1 to allow sufficient time for customers to prepare for upcoming KBL-R workshop in TW in ww24/25.

Intel will release version 2.4.0 in ww22/ww23 which will contain both sets of update for KBL-R (in rev 2.2.1) and CFL-S (in rev 2.3.0).

Customers coming to KBL-R workshop in Taiwan in ww24/25 are required to have 2.2.1 integrated. Customers also can come with 2.4.0 to workshop if they have time for integration and wish to use single BIOS. Customers cannot use 2.3.0 for the KBL-R PV or workshop in ww24/ww25 since 2.3.0 doesn't have required KBL-R changes.

This release includes source code (including debug code) and APIs. Integration guide is part of SiPkgApi.chm (Doxygen) file now. Please refer to the help file for all code integration needs.

Refer to [BIOS Specification Information.txt](#) (under Doc folder) for BIOS specifications that support this release.

This release is based on BIOS 85.8.

2.44 Version 2.3.0

This release adds Coffee Lake support to the code.

This release includes source code (including debug code) and APIs. Integration guide is part of SiPkgApi.chm (Doxygen) file now. Please refer to the help file for all code integration needs.



Refer to [BIOS Specification Information.txt](#) (under Doc folder) for BIOS specifications that support this release.

Notes: To reduce impact on KabyLake-R PV milestones, Intel will release updates to Kaby-Lake-R based on Si Code version 2.2.0. The versioning of these releases will be 2.2.1, 2.2.2, etc. These updates will include only required changes for KBL-R PV. Notice that all KBL-R changes are also part of 2.3.0 Si code. Once KBL-R is Pved, all future KBL-R updates will be released as part of the main release (i.g. 2.5.0, 2.6.0 etc.).

This release is based on BIOS 89.

2.45 **Version 2.2.0**

This release is for KBL-R update and production platforms update.

This release includes source code (including debug code) and APIs. Integration guide is part of SiPkgApi.chm (Doxygen) file now. Please refer to the help file for all code integration needs.

Refer to [BIOS Specification Information.txt](#) (under Doc folder) for BIOS specifications that support this release.

This release is based on BIOS 85.2.

2.46 **Version 2.1.0**

This release is for KBL-R Beta milestone and production platforms update.

This release includes source code (including debug code) and APIs. Integration guide is part of SiPkgApi.chm (Doxygen) file now. Please refer to the help file for all code integration needs.

Refer to [BIOS Specification Information.txt](#) (under Doc folder) for BIOS specifications that support this release.

This release is based on BIOS 83.1.

2.47 **Version 2.0.0**

This release is for KBL-R Beta milestone and production platforms update.

This release includes source code (including debug code) and APIs. Integration guide is part of SiPkgApi.chm (Doxygen) file now. Please refer to the help file for all code integration needs.

Refer to [BIOS Specification Information.txt](#) (under Doc folder) for BIOS specifications that support this release.

This release is based on BIOS 81.1.



2.48 Version 1.9.0

This release is for KBL-R Pre-Beta milestone and production version update. This release includes source code (including debug code) and APIs. Integration guide is part of SiPkgApi.chm (Doxygen) file now. Please refer to the help file for all code integration needs.

Refer to [BIOS Specification Information.txt](#) (under Doc folder) for BIOS specifications that support this release.

This release is based on BIOS 78.

2.49 Version 1.8.0

This release is for KBL-R pre-Beta milestone and production version update.

This release includes source code (including debug code) and APIs. Integration guide is part of SiPkgApi.chm (Doxygen) file now. Please refer to the help file for all code integration needs.

Refer to [BIOS Specification Information.txt](#) (under Doc folder) for BIOS specifications that support this release.

This release is based on BIOS v77/77.1.

2.50 Version 1.7.0

This release is for Pre-ES KBL-R and production version update to PR2.

This release includes source code (including debug code) and APIs. Integration guide is part of SiPkgApi.chm (Doxygen) file now. Please refer to the help file for all code integration needs.

Refer to [BIOS Specification Information.txt](#) (under Doc folder) for BIOS specifications that support this release.

This release is based on BIOS v73.3.

2.51 Version 1.6.0

This release is production version update to PR2.

This release includes source code (including debug code) and APIs. Integration guide is part of SiPkgApi.chm (Doxygen) file now. Please refer to the help file for all code integration needs.

Refer to [BIOS Specification Information.txt](#) (under Doc folder) for BIOS specifications that support this release.

This release is based on BIOS v72.1.



2.52 Version 1.5.0

This release is Production Version for Skylake on Kaby lake Platforms with windows 7/8.1 production version update (PR2) for Spring Refresh 2017, and update for U23e. This release includes source code (including debug code) and APIs. Integration guide is part of SiPkgApi.chm (Doxygen) file now. Please refer to the help file for all code integration needs.

Refer to [BIOS Specification Information.txt](#) (under Doc folder) for BIOS specifications that support this release.

This release is based on BIOS v65.5.

2.53 Version 1.4.1

This release is provides update for **Optane development/testing only**.

This release includes source code (including debug code) and APIs. Integration guide is part of SiPkgApi.chm (Doxygen) file now. Please refer to the help file for all code integration needs.

Refer to [BIOS Specification Information.txt](#) (under Doc folder) for BIOS specifications that support this release.

This release is based on BIOS v65.1.

NOTE: This code is not recommended to be used in production/ manufacturing environment. Please refer to BKC's for appropriate Si Init code and BIOS used for latest milestones.

2.54 Version 1.4.0

This release is Production Version for Skylake on Kaby lake Platforms with windows 7/8.1 production version update (PR1) for Spring Refresh 2017, and update for U23e.

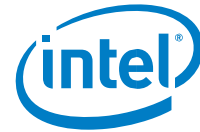
This release includes source code (including debug code) and APIs. Integration guide is part of SiPkgApi.chm (Doxygen) file now. Please refer to the help file for all code integration needs.

Refer to [BIOS Specification Information.txt](#) (under Doc folder) for BIOS specifications that support this release.

This release is based on BIOS v62.3.

2.55 Version 1.3.0

This release is Production Version for Spring Refresh 2017 H Consumer, Production Candidate/Production Version* for Spring Refresh 2017 PC/PV SR'17 S42/H/ and Y/U22 – Corporate*.



This release includes source code (including debug code) and APIs. Integration guide is part of SiPkgApi.chm (Doxygen) file now. Please refer to the help file for all code integration needs.

Refer to [BIOS Specification Information.txt](#) (under Doc folder) for BIOS specifications that support this release.

This release is based on BIOS v52.12.

NOTE*: No Silicon Code Changes are expected between Corporate PC and PV.

2.56 Version 1.2.0

This release is Production Version for Spring Refresh 2017 S Consumer, PC for Spring Refresh 2017 U/Y and Update for other SKUs.

This release includes source code (including debug code) and APIs. Integration guide is part of SiPkgApi.chm (Doxygen) file now. Please refer to the help file for all code integration needs.

Refer to [BIOS Specification Information.txt](#) (under Doc folder) for BIOS specifications that support this release.

This release is based on BIOS v52.7/52.9.

2.57 Version 1.1.0

This release is Production Version for Holiday Refresh with RS1. This release contains update for Spring Refresh 2017 U/Y/S/H and Update for U32e.

This release includes source code (including debug code) and APIs. Integration guide is part of SiPkgApi.chm (Doxygen) file now. Please refer to the help file for all code integration needs.

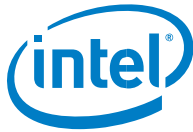
Refer to [BIOS Specification Information.txt](#) (under Doc folder) for BIOS specifications that support this release.

This release is based on BIOS v52.4.

2.58 Version 1.0.5

This release is update for Holiday Refresh with RS1 and update for Spring Refresh 2017 U/Y/S/H and Update for U32e.

This release includes source code (including debug code) and APIs. Integration guide is part of SiPkgApi.chm (Doxygen) file now. Please refer to the help file for all code integration needs.



Refer to [BIOS Specification Information.txt](#) (under Doc folder) for BIOS specifications that support this release.

This release is based on BIOS v52.2.

2.59 **Version 1.0.4**

This release is Holiday Refresh with RS1 and Spring Refresh 2017 U/Y/S/H /U32e update.

This release includes source code (including debug code) and APIs. Integration guide is part of SiPkgApi.chm (Doxygen) file now. Please refer to the help file for all code integration needs.

Refer to [BIOS Specification Information.txt](#) (under Doc folder) for BIOS specifications that support this release.

This release is based on BIOS v50.2.

2.60 **Version 1.0.3.1**

This release update copyright header.

The only change in this release is related to copyright header of each file. This release is provided w/o any other change and is not required for customers to integrate. The updated copyright header will be carry over into future release of the code. Intel recommends that any future code releases be compared to this release to eliminate false number of file changed.

This release is based BIOS v47.1, same as 1.0.3 release below.

2.61 **Version 1.0.3**

This release is Spring Refresh 2017 U/Y/S/H Beta update.

This release includes source code (including debug code) and APIs. Integration guide is part of SiPkgApi.chm (Doxygen) file now. Please refer to the help file for all code integration needs.

Refer to [BIOS Specification Information.txt](#) (under Doc folder) for BIOS specifications that support this release.

This release is based on BIOS v47.1.

2.62 **Version 1.0.2**

This release is Spring Refresh 2017 S/H/Y/U Beta.

This release includes source code (including debug code) and APIs.



Integration guide is part of SiPkgApi.chm (Doxygen) file now. Please refer to the help file for all code integration needs.

Refer to [BIOS Specification Information.txt](#) (under Doc folder) for BIOS specifications that support this release.

This release is based on BIOS v45.1.

2.63 Version 1.0.1

This release is U/Y Holiday Refresh 2016 with RS1, U23e Alpha with RS1 and Spring Refresh 2017 S/H/Y/U Beta.

This release includes source code (including debug code) and APIs. Integration guide is part of SiPkgApi.chm (Doxygen) file now. Please refer to the help file for all code integration needs.

Refer to [BIOS Specification Information.txt](#) (under Doc folder) for BIOS specifications that support this release.

This release is based on BIOS v43.2.

2.64 Version 1.0.0

This release is U/Y Production Version. It also includes DT Alpha Cons/Corp update and Halo Alpha Cons/Corp update.

This release includes source code (including debug code) and APIs. Integration guide is part of SiPkgApi.chm (Doxygen) file now. Please refer to the help file for all code integration needs.

Refer to [BIOS Specification Information.txt](#) (under Doc folder) for BIOS specifications that support this release.

This release is based on BIOS v038.60.

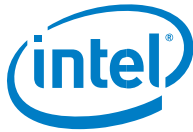
2.65 Version 0.9.1

This release is for U/Y Production Candidate, DT with SPT Alpha Cons/Corp update and Halo Alpha Cons/Corp update.

This release includes source code (including debug code) and APIs. Integration guide is part of SiPkgApi.chm (Doxygen) file now. Please refer to the help file for all code integration needs.

Refer to [BIOS Specification Information.txt](#) (under Doc folder) for BIOS specifications that support this release.

This release is based on BIOS v038.50.



2.66 **Version 0.9.0**

This release is for U/Y PC candidate, DT with SPT Alpha Cons/Corp update and Halo Alpha Cons/Corp update.

This release includes source code (including debug code) and APIs. Integration guide is part of SiPkgApi.chm (Doxygen) file now. Please refer to the help file for all code integration needs.

Refer to [BIOS Specification Information.txt](#) (under Doc folder) for BIOS specifications that support this release.

This release is based on BIOS v038.15.

2.67 **Version 0.8.1**

This release is for U/Y Beta Update, DT with SPT Alpha Cons/Corp update and Halo Alpha Cons/Corp update.

This release includes source code (including debug code) and APIs. Integration guide is part of SiPkgApi.chm (Doxygen) file now. Please refer to the help file for all code integration needs.

Refer to [BIOS Specification Information.txt](#) (under Doc folder) for BIOS specifications that support this release.

This release is based on BIOS v038.

2.68 **Version 0.8.0**

This release is for U/Y Beta Update, DT with SPT Alpha Cons/Corp update and Halo Alpha Cons/Corp update.

This release includes source code (including debug code) and APIs. Integration guide is part of SiPkgApi.chm (Doxygen) file now. Please refer to the help file for all code integration needs.

Refer to [BIOS Specification Information.txt](#) (under Doc folder) for BIOS specifications that support this release.

This release is based on BIOS v036.2.

2.69 **Version 0.7.3**

This release is update for U/Y Beta Update, DT with SPT Alpha Cons/Corp and Halo Alpha Cons/Corp.

This release includes source code (including debug code) and APIs.

Integration guide is part of SiPkgApi.chm (Doxygen) file now. Please refer to the help file for all code integration needs.



Refer to [BIOS Specification Information.txt](#) (under Doc folder) for BIOS specifications that support this release.

This release is based on BIOS v033.3.

Note: while Si code this release covers multiple milestones, the BKC for these milestones are targeted for release between ww15 and ww16. Please ensure you are using right BKC for appropriate milestone.

2.70 Version 0.7.2

This release is update for U/Y Beta Update.

This release includes source code (including debug code) and APIs.

Integration guide is part of SiPkgApi.chm (Doxygen) file now. Please refer to the help file for all code integration needs.

Refer to [BIOS Specification Information.txt](#) (under Doc folder) for BIOS specifications that support this release.

This release is based on BIOS v031.1.

2.71 Version 0.7.1

This release is update for U/Y Beta Update.

This release includes source code (including debug code) and APIs.

Integration guide is part of SiPkgApi.chm (Doxygen) file now. Please refer to the help file for all code integration needs.

Refer to [BIOS Specification Information.txt](#) (under Doc folder) for BIOS specifications that support this release.

This release is based on BIOS v028.1.

2.72 Version 0.7.0

This release is update for U/Y Beta.

This release includes source code (including debug code) and APIs.

Integration guide is part of SiPkgApi.chm (Doxygen) file now. Please refer to the help file for all code integration needs.

Refer to [BIOS Specification Information.txt](#) (under Doc folder) for BIOS specifications that support this release.

This release is based on BIOS v025.2



2.73 Version 0.6.1

This release is update for U/Y Alpha.

This release includes source code (including debug code) and APIs.

Integration guide is part of SiPkgApi.chm (Doxygen) file now. Please refer to the help file for all code integration needs.

Refer to [BIOS Specification Information.txt](#) (under Doc folder) for BIOS specifications that support this release.

This release is based on BIOS v022.1

2.74 Version 0.6.0

This release is update for U/Y Alpha.

This release includes source code (including debug code) and APIs.

Integration guide is part of SiPkgApi.chm (Doxygen) file now. Please refer to the help file for all code integration needs.

Refer to [BIOS Specification Information.txt](#) (under Doc folder) for BIOS specifications that support this release.

This release is based on BIOS v020

2.75 Version 0.5.0

Initial silicon power-on and SW Alpha milestone release

This release can be used for silicon power-on and U/Y Alpha activates.

This release includes source code (including debug code) and APIs.

Integration guide is part of SiPkgApi.chm (Doxygen) file now. Please refer to the help file for all code integration needs.

Refer to [BIOS Specification Information.txt](#) (under Doc folder) for BIOS specifications that support this release.

3 Reference Code Version Decoder

3.1 Version Decoder Priory First PV

Pre first/TTM Platform PV release versioning decoded is:



- BKM follows the lead silicon PV (first TTM SKU)
- Support for other silicon/platforms are added at the appropriate time and their enabling does not follow the BKM below
- Version decoder - X.Y.Z.w (example 0.5.0 or 0.7.1.0)
 - X Major/Platform Milestone (0 = pri first PV)
 - Y First/Lead TTM platform milestones (example 0.7.0 Beta for MB HR'16 – first TTM platforms)
 - Z BKC releases
 - w code re-release or built update

Ref Code version	Lead /TTM Platform Milestone	Lead Silicon
0.5.0		Pre-power on code
0.5.x	Pre-Alpha	PreES
0.6.0	Alpha	ES
0.7.0	Beta	
0.8.0		QS
0.9.0	PC	
1.0.0	First/TTM PV	Fist TTM Silicon

- Always refer to customer dashboard for latest scheduled release of BIOS collaterals

3.2 Version Decoded Post First/TTM Platform PV

Post 1.0.0 release follow below versioning decoder and may include updates or milestones for post first/TTM platform.

Version decoder - X.Y.Z.w (example 1.0.5 or 1.1.0.0)

- X Major/Platform Milestone (0 = pri-PV, 1 = First/TTM PV)
 - Example 1.0.0 PV HR'16 for TH2
- Y Post first/TTM platform PVs



Reference Code Version Decoder

- 1.1.Z = Second platform PV (driven by Silicon PV or OS PV) ○
- 1.2.Z = Third platform PV (driven by Silicon PV or OS PV)
 - Example 1.1.0 HR'16/RS1 PV
- Z Post first/TTM platform PVs BKC releases
 - Example 1.0.1 BKC release for Beta for S/H SR'17
 - Example 1.0.2 BKC release/Update for All
- w code re-release or built update
- Always refer to customer dashboard for latest scheduled release of BIOS collaterals